Superconducting single-photon detector made of MoSi film

Yu P Korneeva¹, M Yu Mikhailov², Yu P Pershin⁵, N N Manova¹, A V Divochiy³, Yu B Vakhtomin³, A A Korneev^{1,4,6}, K V Smirnov³, A G Sivakov², A Yu Devizenko⁵ and G N Goltsman^{1,6}

E-mail: korneeva_yuliya@mail.ru

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Abstract

We fabricated and characterized nanowire superconducting single-photon detectors made of 4 nm thick amorphous Mo_xSi_{1-x} films. At 1.7 K the best devices exhibit a detection efficiency (DE) up to 18% at 1.2 μ m wavelength of unpolarized light, a characteristic response time of about 6 ns and timing jitter of 120 ps. The DE was studied in wavelength range from 650 nm to 2500 nm. At wavelengths below 1200 nm these detectors reach their maximum DE limited by photon absorption in the thin MoSi film.

Keywords: superconducting single photon detectors, thin films, detection efficiency, response time, timing jitter

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(Some figures may appear in colour only in the online journal)

1. Introduction

Recently the use of nanowire superconducting single-photon detectors (SSPDs) [1] was demonstrated in many applications ranging from quantum key distribution [2] to time-of-flight depth ranging [3]. While NbN was originally the first material used for SSPDs many other materials were tested during the past years such as Nb [4], NbTiN [5], TaN [6], MgB₂ [7], NbSi [8], W_xSi_{1-x} [9], and $Mo_{0.75}Ge_{0.25}$ [10]. Among them the SSPDs made of amorphous W_xSi_{1-x} (a-WSi) films turned out to be very promising. These SSPDs with also an improved optical coupling demonstrated at 120 mK up to 93% system detection efficiency (DE) at a wavelength near 1550 nm [11] and 75% DE with 300 dark counts per second at 2.5 K [12] (supplementary material). Amorphous WSi is claimed to be attractive due to its critical temperature T_c tunable over a wide range by the film stoichiometry, larger hot spot compared to NbN (for the same photon energy and film thickness), structural homogeneity and absence of grain boundaries. Due to high DE in the wavelength range of 1280–1650 nm they are expected to be usable for the midinfrared wavelengths [9]. Besides, amorphous films do not set strict requirements on the substrate crystal quality. This fact enables one to fabricate a cavity-integrated detector with the mirror placed on the substrate and front-side illumination of the detector (called 'Type I' device in our previous work [13]).

Despite of the fact that by now a number of materials, other than NbN, have been studied, an optimally suitable material has not been found. For example, for some materials T_c is too low. While the maximum reported T_c of thick a-WSi films is 5 K [14], the nanowire made of the 4.5 and 4 nm thick a-WSi film has a T_c of 3 K [9] and 3.7 K [11]. For a-NbSi SSPDs a critical temperature near 2 K was reported [8]. On another aspect, the single photon response of a-WSi SSPDs showed a saturation in internal DE at temperatures below 2 K

¹ Moscow State Pedagogical University, 1 Malaya Pirogovskaya str., 119991 Moscow, Russia

² B. Verkin Institute for Low Temperature Physics and Engineering of the National Academy of Sciences of Ukraine, 47 Lenina ave., 61103 Kharkiv, Ukraine

³CJSC 'Superconducting nanotechnology' (Scontel), 5/22 Rossolimo str., 119991 Moscow, Russia

⁴ Russian Quantum Center, 100 Novaya str., Skolkovo, Moscow Region, Russia

⁵ National Technical University 'Kharkiv Polytechnic Institute', 21 Frunze str., 61002 Kharkiv, Ukraine

⁶ National Research University Higher School of Economics, 20 Myasnitskaya Ulitsa, Moscow 101000, Russia

for 1.55 μ m radiation. However, the critical current of a-WSi SSPDs is only $2 \mu A$ at 2 K. In addition the best performing devices were operated at a temperature as low as 120 mK (figure 3 of [11]). In this paper we study SSPDs made of another refractory metal-silicon alloy: amorphous Mo_xSi_{1-x} (a-MoSi). In comparison, the maximum T_c of a-MoSi films is higher compared to the T_c of a-WSi and a-NbSi. A T_c of 7.5 K is reported for 25 nm thick a-Mo_{0.75}Si_{0.25} film [15], and of 7.3 K for a 50 nm thick a-Mo_{0.80}Si_{0.20} film [16]. Using a-MoSi we intend to provide higher temperatures for efficient SSPD operation ($T \sim 1.7$ K) compared to a-WSi SSPDs, achievable by helium vapour pumping from a cryostat or a cryoinsert for storage Dewar as described in Korneev et al [17]. Our aim is to have excellent performance at temperatures above 1 K. Therefore we have chosen to take a material with analogous properties as WSi but with a higher T_c . We expect in the temperature range of T > 1 K a higher critical current than in a-WSi, a lower response time and a lower timing jitter compared to a-WSi. Here we report our measurements of the DE, dark count rate, and timing characterisation of a-MoSi SSPD detectors made of films with Si content of 25% and 20% hereafter referred to as a-Mo_{0.75}Si $_{0.25}$ and a-Mo_{0.80}Si_{0.20}.

2. Device fabrication and experimental set-up

The 4 nm thick a-MoSi films are deposited by dc magnetron sputtering from molybdenum and silicon targets on polished and thermally oxidized (100) Si wafers. SiO₂ layer is 250 nm thick. No intentional substrate heating is used and the substrate temperature does not exceed 100 °C during the sputter deposition. We evaluate the stoichiometry of the films from the calibrated deposition rates of Mo and Si. For constant settings of the deposition conditions the film thickness is controlled via the deposition time. The thickness of the films is checked by x-ray reflectivity measurements and their amorphous structure is confirmed by x-ray diffraction. The films are capped with a 3 nm thick Si layer to prevent oxidation. Being partially oxidized in the natural way, this layer has a resistivity of about 2Ω cm which is by 4 order of magnitude higher compared to a-MoSi resistivity. Thus we do not expect any influence of this caping layer on the electrical properties of a-MoSi. The patterning procedure is essentially the same as for the NbN detectors [18]: the film is patterned into meanders covering $7 \mu m \times 7 \mu m$ area by e-beam lithography in a positive-tone resist ZEP 520A7 and reactive ion etching in a SF₆ plasma. The meander strip width varied for different devices from 110 nm to 130 nm with the gap between the strips varying from 70 nm to 90 nm. After patterning of the meanders, the Cr/Cu electrical contacts are fabricated by electron beam evaporation and patterned by photolithography method. Taking into account the large area of the contact pads (about 3 mm²) there are inevitably holes in the Si capping layer that provided a good contact between Cr/ Cu and a-MoSi. Such holes also might appear after ultrasonic bonding or by spring pins when the sample is mounted in the measurement dipstick.

The resistivity of a-MoSi is calculated from the sheet resistance R_s and film thickness d. The sheet resistance is measured on a piece of wafer with unpatterned film by a four-point in-line probe technique using a commercial wafer probe: current I is supplied by an outer pair of probes, voltage U is measured across the two inner probes. If the probes are equidistant the sheet resistance can be calculated according to: $R_s = CU/I$, where the correction factor C accounts for the shape and size of the sample [19]. The R_s calculated from the sheet resistance of unpatterned film is in a reasonable agreement with R_s calculated from patterned detector resistance, film thickness, nanowire width and length. Any observed descrepacies can be explained by inaccuracy in nanowire width measurement.

For DE measurement the detector is mounted in an dipstick placed inside a cryoinsert for a storage dewar enabling operation at 1.7 K by evacuation of the helium vapour from the cryoinsert. The detector is illuminated with unpolarized light of $1.2\,\mu\mathrm{m}$ wavelength from a light emitting diode (LED). The light is delivered to the detector by a bunch of multimode fibres which provide uniform illumination of the detector. Additional uniformity is achieved by placing the SSPD chip at a distance of about 1 mm from the end of the fibre bunch. The SSPD is illuminated from the front side. The DE is defined as the ratio of photon counts to the number of incoming photons

$$DE = \frac{N_c - N_{drk}}{N_{ph}},$$
 (1)

where N_c is the number of counts when the LED is on, N_{drk} is the number of dark counts when the LED is off, $N_{\rm ph}$ is the number of incoming photons. In our experiment the dark count rate with the LED off is equal to the dark count rate with the fibre input blocked by a black cap. To determine the number of photons incident on the detector we place a pinhole at the same position, where normally SSPD is located, and measure the optical power through the pinhole. Knowing the ratio of the areas of the pinhole and the a-MoSi meander we calculate the number of photons incident on the meander area. Since the radiation wavelength is several times larger than the gaps between the strips of the meander we do not take these gaps into account when we calculate the meander area, thus we take the whole area $7 \mu \text{m} \times 7 \mu \text{m}$. The results of this method are consistent with the results of DE measurements for SSPDs subsequently packaged with single-mode fibres.

In the procedure above we use a definite wavelength. To determine the wavelength dependence of the DE we use a grating monochromator producing unpolarized light which is delivered to the SSPD by the same fibre bunch as we used for DE measurement. The output power of the monochromator is calibrated with the Golay cell. All the rest of the setup is essentially the same as described above.

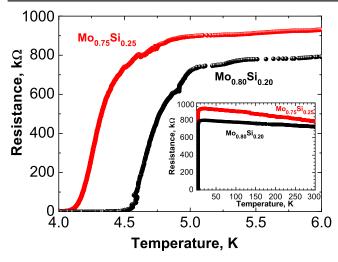


Figure 1. Comparison of the resistance versus temperature for SSPDs with 120 nm wide strips made of $Mo_{0.75}Si_{0.25}$ (red) and Mo $_{0.80}Si_{0.20}$ (black). The inset shows the resistance versus temperature for the same devices in the temperature range up to 300 K.

3. Results and discussion

3.1. Resistance versus temperature

We measured the temperature dependence of the resistance for the meanders. We observe wide transition due to film disorder as shown in figure 1. The devices and the unpatterned films have a small negative temperature coefficient of resistivity as shown in the inset of figure 1. The ratio of the resistances at 300 K and 20 K temperatures R_{300}/R_{20} is 0.85 and 0.90 for a-Mo_{0.75}Si_{0.25} and a-Mo_{0.80}Si_{0.20} devices respectively. The electrical resistivity of unpatterned films is about 220 $\mu\Omega$ cm for a-Mo_{0.75}Si_{0.25} and 185 $\mu\Omega$ cm for a-Mo_{0.80}Si_{0.20} at 10 K which is in accordance with previously published data for amorphous a-MoSi [16] and a-WSi films [14].

3.2. DE and dark count rate

Figure 2 presents DE for one of the best devices measured at $1.2 \,\mu\text{m}$ wavelength of unpolarized light. This device showed critical current of $6.8 \,\mu\text{A}$ at $1.7 \,\text{K}$. One can see that at bias current above $0.8 \,I_{\rm c}$ DE exhibits somewhat like a beginning of a plateau, which is much better pronounced compared to NbN devices at the same wavelength and temperature, but yet it is not so clear as for WSi devices [9].

The dark count rate is presented in the inset of figure 2. The plateau at count rates below 10 counts per second and bias current below $0.8\,I_{\rm c}$ is caused by residual light and the electrical noise of the setup. The electrical noise is hard to filter due to poor signal-to-noise ratio.

Figure 3 presents histograms of DE (a, b) and critical current (c, d) measured for a-Mo_{0.75}Si_{0.25} (a, c) and a-Mo_{0.80}Si_{0.20} (b, d) devices at 1.7 K. One can see that both materials are quite similar. Devices made from a-Mo_{0.80}Si_{0.20} film exhibit higher critical currents which may be caused by higher T_c . Although one can see that a-Mo_{0.75}Si_{0.25} devices exhibit

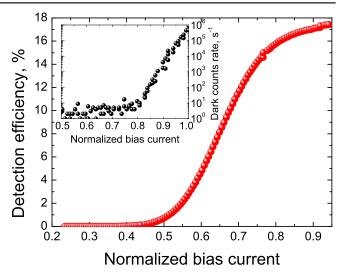


Figure 2. Detection efficiency for one of the best devices measured at 1.7 K exhibits a beginning of a plateau at bias current close to $I_{\rm c}$ ($I_{\rm c}=6.8\,\mu{\rm A}$). The inset shows dark count rate for the same device measured at the same temperature. The plateau in dark count at bias current below $0.8\,I_{\rm c}$ is caused by residual light and the electrical noise of the setup.

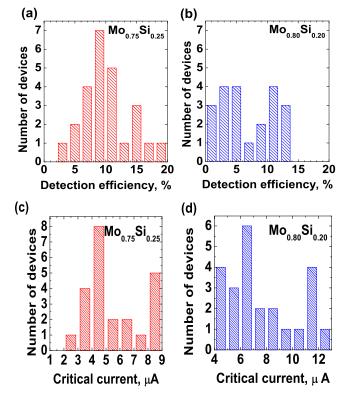


Figure 3. Histograms of DE (a, b) and critical current (c, d) measured for a-Mo $_{0.75}$ Si $_{0.25}$ (a, c) and a-Mo $_{0.80}$ Si $_{0.20}$ (b, d) devices at 1.7 K.

better fabrication yield and higher DE we do not have enough statistical data to claim that it is the property of material itself and not the imperfection during the fabrication process.

Figure 4 presents DE versus wavelength for a-Mo_{0.75}Si $_{0.25}$ SSPD in range 650 nm–2500 nm at bias currents 0.93 $I_{\rm c}$ (red points) and 0.79 $I_{\rm c}$ (blue points). The top black curve is the simulated absorption of multilayer stack consisting of

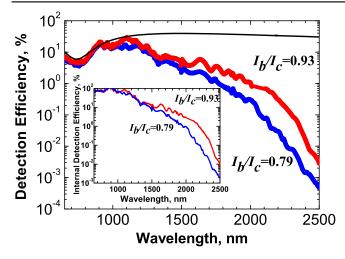


Figure 4. Detection efficiency versus wavelength measured at bias currents $0.93\,I_{\rm c}$ (red curve) and $0.79\,I_{\rm c}$ (blue curve). Top black curve shows simulated absorption of multilayer stack of 3 nm capping Si, a-MoSi detector, SiO₂ and Si. The inset shows internal detection efficiencies of a-MoSi SSPD at bias currents $0.93\,I_{\rm c}$ and $0.79\,I_{\rm c}$.

3 nm thick capping Si, a-MoSi detector, 250 nm thick SiO₂, and Si. The simulation was performed with 1D array transfer matrix method [20]. Optical refractive indices of the dielectric materials (Si and SiO₂) were taken from [21], whereas reflectance of patterned a-MoSi film was calculated from its sheet resistance at 20 K. Our simulation showed that 3 nm capping does not significantly affect the absorption but shifts the wavelengths of maximum and minimum absorption by several nanometers. One can note that at wavelengths shorter than 1000 nm DE is very close to the simulated absorption indicating almost 100% internal DE, i.e. every absorbed photon produces a count. This is clearly indicated in the inset of figure 4 where we plotted internal DE which is the ratio of measured DE to simulated absorption. A plateau up to 1200 nm wavelength is clearly visible in the internal DE at both bias currents.

3.3. Response time and timing jitter

Figure 5 presents a typical averaged waveform transient for $7 \,\mu\text{m} \times 7 \,\mu\text{m}$ area a-MoSi detector after about 46 dB amplification by two amplifiers Mini-Circuits ZFL-1000LN+ with 0.1-1000 MHz band and noise figure of about 6 dB. We evaluate the influence of the kinetic inductance on the response fall time by fabricating meanders of two more sizes: $3 \mu \text{m} \times 3 \mu \text{m}$, and $10 \mu \text{m} \times 10 \mu \text{m}$. As it was shown in [22] the response fall time τ_f of SSPD is limited by the kinetic inductance of the wire L_k : $\tau_f = L_k/R$, where R is the impedance of the coaxial transmission line which is in most cases (as well as in our case) 50 Ω . Here we define the response time as the time required for the voltage to decay by a factor of e (the base of the natural logarithm). We did not measure L_k directly. Instead we followed the approach proposed in [22] and used the fact that L_k has the same dependence on strip length l, width w and film thickness d ([23]) as room

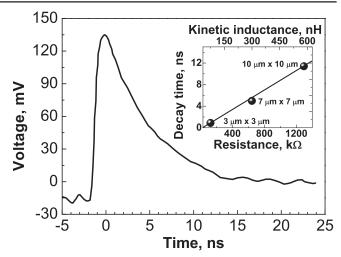


Figure 5. Averaged waveform transient for $7 \mu m \times 7 \mu m$ SSPD. The inset shows the dependence of the response fall time on the detector room temperature resistance. The linear dependence supports the idea that the response time is limited by the kinetic inductance.

temperature resistance R_{300}

$$L_k = \mu_0 \lambda^2 \int_0^l \frac{dx}{w(x)d(x)}$$
 and $R_{300} = \rho \int_0^l \frac{dx}{w(x)d(x)}$, (2)

where λ is magnetic field penetration depth, ρ is the resistivity of the film material at room temperature. This approach gives a better accuracy because even if there is a variation of l, w, and d, L_k remains proportional to R_{300} .

The inset in figure 5 shows the response fall time versus room temperature resistance for the meanders of different lengths. One can see that the response time is close to a linear dependence suggesting that even for $3 \mu m \times 3 \mu m$ devices the recovery time is still limited by the kinetic inductance. Taking into account the geometry of our devices we calculated a kinetic inductance of a square unit of our device $L_{\square} = 0.15 \, \text{nH}$. This is rather close to NbN meanders ($L_{\square} = 0.12 \, \text{nH}$ for SSPD reported in [24]) and approximately by a factor of 2 smaller than L_{\square} of WSi reported in [11] (taking 120 ns decay time between 90% and 10% levels, $15 \, \mu \text{m} \times 15 \, \mu \text{m}$ detector size, 120 nm strip width and 200 nm pitch we obtained $L_{\square} = 0.3 \, \text{nH}$ for WSi reported in [11]).

We determine the timing jitter of the a-MoSi SSPDs from the width of the coincidence histogram taken at the half of its maximum. The jitter is measured for different bias currents ranging from $5.5 \,\mu\text{A}$ to $I_{\rm c}$ which is about $8.3 \,\mu\text{A}$ for this device. The results are presented in figure 6. The timing jitter is decreasing from 190 ps at $5.5 \,\mu\text{A}$ bias current to about 120 ps near $I_{\rm c}$. Such a dependence on bias current is very similar to the one reported for NbN by You *et al* [25] and for WSi in supplementary material of [11]. The agreement of our data and their data suggests that in our case timing jitter is also limited by the noise of the read-out. Indeed, when $I_{\rm c} < 10 \,\mu\text{A}$ the response voltage of the detector is just several times higher than the RMS of the amplifier noise voltage. Although these values of the jitter are not the properties of the material nevertheless they give an idea of what one can obtain

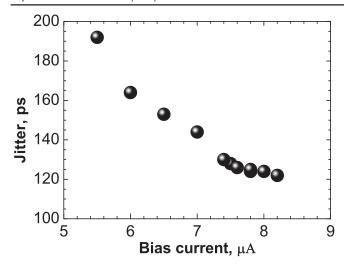


Figure 6. Timing jitter of a-MoSi SSPD measured at different bias currents. Such a dependence of the jitter on bias current together with low response voltage of the detector suggest that in our case jitter is limited by the noise of the read-out electronics similar to [25].

in a practical MoSi single-photon receiver operated with room-temperature amplifiers.

4. Conclusion

We fabricated and tested amorphous a-Mo_xSi_{1-x} SSPDs. These devices exhibited critical temperatures of 4.3–4.9 K allowing their successful operation at 1.7 K temperature. The best devices have maximum DE of 18% at 1.2 μ m wavelength for unpolarized light, and 16% at 10 dark counts per second. We demonstrate a kinetic inductance limited 1/e voltage decay time for a 7μ m × 7μ m detector of about 6 ns with a timing jitter of 120 ps limited by the RMS of the amplifier noise.

Although the best DE we achieve so far is about 18% we believe that this material has high potential for future SSPD development, because it allows the integration with optical cavities enabling front-side illumination.

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